

SCHEDULE A

Patents, Patent Applications and Inventions

Descriptive Invention Title	Invention Date	US Filing Date	Inventor(s)	Textual Status	Docket Number	Serial Number
Method for optimizing acknowledge packet rate	6/1/97	7/21/98	Bill Duckwall	Allowed	ZAY-001	09/121,616
Pipelined arbitration for a bus using dual phases	Q3 '98 ?	11/5/99	Mike Teener	Filed	ZAY-99-012	09/435,134
P1394a Loop Breaking	3/12/99 - 3/16/99	7/9/99	Jerry Hauck Colin Whitby-Stevens	Filed	ZAY-002	09/350,583
P1394b Loop Breaking	4/27/99	11/5/99	Jerry Hauck Colin Whitby-Stevens	Filed	ZAY-99-018	
P1394b Automatic Cross-over of TPA/TPB	4/16/99	4/21/00	Colin Whitby-Stevens	Filed	ZAY-00-019	09/557,073
Dynamic CSR ROM	6/23/98	11/1/99	Steve Brown	Filed	ZAY-99-027	09/431,409
Link Driver Architecture - Link Configuration	3/2/98	11/1/99	Steve Brown	Filed	ZAY-99-029	09/431,703
Device communication path construction	10/1/97	11/2/99	Erik Staats	Filed	ZAY-99-034	09/432,915
Distributed device control over multiple transports	8/1/97	11/1/99	Erik Staats	Filed	ZAY-99-037	09/431,704
Bridging of distributed device control through multiple transports	8/1/97	11/2/99	Erik Staats	Filed	ZAY-99-038	09/432,997
AV/C over multiple transports	1/1/99	2/16/00	Erik Staats	Filed	ZAY-99-039	
Allocation of IDs for AV/C entities	8/1/98	11/2/99	Erik Staats	Filed	ZAY-99-041	09/432,872

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Generic container mechanism for implementing A/V/C descriptors	10/1/98	10/28/99	Erik Staats	Filed	ZAY-99-044	09/429,233
Jitter Test Patterns	4/20/99	4/21/00	Colin Whitby-Stevens	Filed		
Detection of a P1394b Hybrid Bus	6/11/99	1/18/00	Colin Whitby-Stevens Jerry Hauck	Filed	ZAY-99-054	
Dual phase arbitration without timers	3/10/99	3/31/00	Mike Teener David Wooten	Filed	1662-16900	
Protecting D/S Clouds from Beta Packets	8/18/99	2/17/00	Jerry Hauck	Filed	ZAY-99-062	
Link Driver Architecture - Node presentation	8/1/97	11/2/99	Steve Brown	Filed	ZAY-99-065	09/432,869
Accelerated Speed Signal Detection	4/29/99	11/16/99	Bill Duckwall	Filed	ZAY-99-067	
P1394b Loop Breaking Refined	6/14/00		David Wooten Jerry Hauck Colin Whitby-Stevens			
Fly-by and ack-accelerated arbitration for broadcasts	7/13/99		Jerry Hauck Prashant Kanhere Bill Duckwall			
Automatic Avoidance of >62 Node Topologies	3/26/99		Colin Whitby-Stevens			
Optimized P1394a Gap Calculations when Pinging	3/10/99		Jerry Hauck			

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Methods for interoperability with legacy S100 PHYs	10/20/98		Bill Duckwall			
Dynamic compensation for Link's GRANT to Data Delay	7/5/99		Bill Duckwall			
PHY cable event			Steve Brown			
Generational resource locking mechanism			Erik Staats			
Streaming data flow control with error recovery			Erik Staats			
Hierarchical references to AV/C entities			Erik Staats			
Optimizing of Disparity Control Phases	6/17/99		Jerry Hauck Colin Whitby-Stevens			
P1394b Legacy Request	5/16/99		Jerry Hauck			
PHY ID 62 reserved for root	7/30/99		Colin Whitby-Stevens			
Selection and Identification of Preferred Border	7/14/99		Colin Whitby-Stevens Jerry Hauck			
P1394b Scalable Bus Busy Indicator	6/17/99		Jerry Hauck Colin Whitby-Stevens			
Restoring Local Root as BOSS	5/21/99		Jerry Hauck			
BOSS vs OK to grant	5/19/99		Jerry Hauck			
Freeing Beta-Only Nodes of Gap Timers	5/13/99		Jerry Hauck			

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Improved Jitter Test Patterns	9/8/99		Colin Whitby-Stevens			